1 2 3 4	IRELL & MANELLA LLP Morgan Chu – mchu@irell.com (70446) Jonathan Steinberg – jsteinberg@irell.com (98044) Benjamin Hattenbach – bhattenbach@irell.com (186455) 1800 Avenue of the Stars, Suite 900 Los Angeles, California 90067-4276 Telephone: (310) 277-1010				
5	Facsimile: (310) 203-7199				
6	Attorneys for Plaintiff TESSERA, INC.				
7					
8	UNITED STATES DISTRICT COURT				
9	NORTHERN DISTRICT OF CALIFORNIA				
10					
11	TESSERA, INC.,) CASE NO. C 05-04063 CW			
12	Plaintiff,	TESSERA, INC.'S FIRST AMENDED COMPLAINT FOR PATENT			
13	VS.) INFRINGEMENT)			
14 15	ADVANCED MICRO DEVICES, INC., a Delaware corporation, SPANSION INC., a Delaware corporation, SPANSION TECHNOLOGY INC., a Delaware) DEMAND FOR JURY TRIAL))			
16	corporation, and SPANSION LLC, a Delaware limited liability corporation,	,))			
17	Defendants.))			
18)			
19	Plaintiff Tessera, Inc. ("Tessera"), by and through its undersigned attorneys, for its First				
20	Amended Complaint against Defendants Spansion Inc., Spansion Technology Inc., and Spansion				
21	LLC (collectively, "Spansion") and Advanced Micro Devices, Inc. ("AMD"), alleges as follows:				
22	<u>PARTIES</u>				
23	1. Tessera is a Delaware corporation with its principal place of business in San Jose,				
24	California.				
25	2. Tessera is informed and believes, and thereon alleges, that AMD is a Delaware				
26	corporation with its principal place of business in Sunnyvale, California.				
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Civil L.R. 3-2(c) on a district-wide basis.

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GENERAL ALLEGATIONS

to Judge Wilkin. This is an intellectual property action that otherwise is assignable pursuant to

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Tessera is a global leader in the development of semiconductor packaging technology. Semiconductor packages serve as the electrical interface between semiconductor chips and the systems in which they operate. They also protect delicate chips from damage, contamination, and stress resulting from repeated heating and cooling. Tessera's technology allows companies around the world to meet the growing demand for smaller, faster, less expensive and more reliable electronic products, including cutting edge medical devices and critical military defense components. The United States Patent Office has awarded Tessera over 300 patents that

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reflect its innovation and protect the company's investment in the technology it has developed.

More than 50 semiconductor manufacturers and packaging companies such as Intel,

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Texas Instruments, Renesas, Toshiba, Sharp, Hitachi, Matsushita, Fujitsu, Sanyo, Hynix and Samsung have taken licenses to Tessera's patented technology. The rights granted by Tessera

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provide consumers with new generations of electronic products. Tessera's patented innovations

under its patents enable companies like Dell, Compaq, Nokia, Ericsson and Sony to continue to

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have already been incorporated into more than four billion semiconductors sold throughout the

as government agencies and the U.S. Army, to advance our nation's homeland defense and

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world. Tessera's technology also enables defense contractors such as General Dynamics, as well

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20 military preparedness. 21

11. Tessera is informed and believes, and thereon alleges, that at the time this Action 22 was commenced, Spansion LLC was a majority-owned subsidiary of AMD. In 1993, AMD and

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Fujitsu engaged in a joint venture to form Spansion LLC to manufacture flash memory products.

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At that time, Spansion LLC was named Fujitsu AMD Semiconductor Limited ("FASL"). FASL

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manufactured and sold flash memory wafers to AMD and Fujitsu, which converted the flash

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memory wafers into finished memory products by, among other things, assembling them into semiconductor packages using Fujitsu's packaging and manufacturing operations. Fujitsu and

A Registered Limited Liability Law Partnership Including

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the Spansion brand name. Spansion now intends to sell its flash memory products directly to customers as well as through its distributors.

- 17. Tessera is informed and believes, and thereon alleges, that packaging is an integral element of Spansion's products. Spansion offers many of its memory products in ball grid array (BGA) format packages, and also in multi-chip BGA packages ("MCPs"), in which semiconductor chips are oriented in a face-up manner and include Tessera's patented technology.
- 18. In March of 2005, Fujitsu took a license to certain Tessera patented semiconductor integrated circuit packaging technology. Fujitsu wished to use the Tessera patented technology to assemble and sell certain products.
- 19. Tessera is informed and believes, and thereon alleges, that Fujitsu utilizes Tessera's chip-scale packaging (CSP) technology in many semiconductor devices. The technology available to Fujitsu covers a broad range of chip-scale and MCP types, including integrated circuit devices packaged in "face-down," "face-up," "fold-over," "stacked," and "system-in-package" (SiP) formats.
- 20. Tessera is informed and believes, and thereon alleges, that the Defendants became aware of the Tessera Patents at least as early as, and possibly earlier than, December 11, 2003. Despite the limited nature of AMD's license, and Spansion's lack of any license whatsoever to practice any of Tessera's patented technology, the Defendants have, with full knowledge of Tessera's patents, made, and continue to make, extensive and profitable use of Tessera's patented face-up packaging advances without fairly compensating Tessera's shareholders. Unlike the many companies that behaved responsibly and with respect for Tessera's United States patents, the Defendants have chosen a very different and illegal course of action. Tessera has accordingly been forced to bring this action.

THE PATENTS-IN-SUIT

21. United States Patent No. 5,679,977 ("'977 patent"), entitled "Semiconductor Chip Assemblies, Methods Of Making Same And Components For Same" was duly and legally issued on October 21, 1997. Tessera is the owner by assignment of all right, title and interest in and to the '977 patent. A copy of the '977 patent is attached as Exhibit A.

1	22.	United States Patent No. 5,852,326 ("'326 patent"), entitled "Face-Up	
2	Semiconductor Chip Assembly" was duly and legally issued on December 22, 1998. Tessera is		
3	the owner by assignment of all right, title and interest in and to the '326 patent. A copy of the		
4	'326 patent is attached as Exhibit B.		
5	23.	United States Patent No. 6,433,419 ("'419 patent"), entitled "Face-Up	
6	Semiconductor Chip Assemblies" was duly and legally issued on August 13, 2002. Tessera is the		
7	owner by assignment of all right, title and interest in and to the '419 patent. A copy of the '419		
8	patent is attached as Exhibit C.		
9	24.	United States Patent No. 6,465,893 ("'893 patent"), entitled "Stacked Chip	
10	Assembly" was duly and legally issued on October 15, 2002. Tessera is the owner by assignment		
11	of all right, title and interest in and to the '893 patent. A copy of the '893 patent is attached as		
12	Exhibit D.		
13	25.	The '977 patent, '326 patent, '419 patent and '893 patent are hereafter referred to	
14	as the "Tessera Patents."		
15	FIRST CAUSE OF ACTION (Patent Infringement By AMD)		
16	26.	Tessera realleges and incorporates by reference paragraphs 1 through 25 as if set	
17	forth herein in	full.	
18	27.	Tessera is informed and believes, and thereon alleges, that in violation of 35 U.S.C.	
19	§ 271 AMD has been and is currently directly infringing, contributorily infringing and/or inducing		
20	infringement of the Tessera Patents by, among other things, making, using, offering to sell, selling		
21	and/or importing without authority or license from Tessera infringing semiconductor components		
22	and assemblies, including flash memory devices having BGA and MCP packages with		
23	semiconductor chips arranged in a face-up orientation.		
24	28.	Tessera is informed and believes, and thereon alleges, that AMD's infringement of	
25	the Tessera Pa	tents has been and continues to be willful.	
26	29.	Unless enjoined, AMD will continue to infringe the Tessera Patents, and Tessera	
27	will suffer irreparable injury as a direct and proximate result of AMD's conduct.		
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1	30. Tessera has been damaged by AMD's conduct, and until an injunction issues wi	11		
2	continue to be damaged in an amount yet to be determined.			
3	SECOND CAUSE OF ACTION			
4	(Patent Infringement By Spansion)			
5	31. Tessera realleges and incorporates by reference paragraphs 1 through 25 as if se	t		
6	forth herein in full.			
7	32. Tessera is informed and believes, and thereon alleges, that in violation of 35 U.S.	S.C.		
8	§ 271 Spansion has been and is currently directly infringing, contributorily infringing and/or			
9	inducing infringement of the Tessera Patents by, among other things, making, using, offering to			
10	sell, selling and/or importing without authority or license from Tessera infringing semiconductor			
11	components and assemblies, including flash memory devices having BGA and MCP packages			
12	with semiconductor chips arranged in a face-up orientation.			
13	33. Tessera is informed and believes, and thereon alleges, that Spansion's infringem	ent		
14	of the Tessera Patents has been and continues to be willful.			
15	34. Unless enjoined, Spansion will continue to infringe the Tessera Patents, and			
16	Tessera will suffer irreparable injury as a direct and proximate result of Spansion's conduct.			
17	35. Tessera has been damaged by Spansion's conduct, and until an injunction issues	•		
18	will continue to be damaged in an amount yet to be determined.			
19	PRAYER FOR RELIEF			
20	WHEREFORE, Tessera requests that this Court enter a judgment as follows:			
21	A. Judgment that the Tessera Patents are valid and enforceable;			
22	B. Judgment that AMD and Spansion infringe each of the Tessera Patents and that			
23	their infringement is, and has been, deliberate and willful;			
24	C. Judgment that this case is exceptional in accordance with 35 U.S.C. § 285;			
25	D. An order that preliminarily and permanently enjoins AMD and Spansion, and the	eir		
26	directors, officers, employees, attorneys, agents and all persons in concert or participation with			
27	any of the foregoing from further acts that infringe, contributorily infringe or induce infringement			
28	of the Tessera Patents;			

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1	E.	E. An order that awards Tessera recovery for all damages that result from the		
2	Defendants' infringing acts;			
3	F.	An order that trebles the amount of damages as a result of the willful and deliberate		
4	nature of the	nature of the Defendants' conduct;		
5	G.	An order that awards Tessera interest on damages;		
6	H.	An order that directs the Defendants to pay Tessera's costs, expenses and attorney		
7	fees in accord	ance with 35 U.S.C. §§ 2	284 and 285 and Rule 54(d) of the Federal Rules of Civil	
8	Procedure; an	d		
9	I.	An order for such other	and further relief as this Court deems just and proper.	
10	DATED: Dec	cember 16, 2005	Respectfully Submitted,	
11			IDELL CAMANELLA LLD	
12			IRELL & MANELLA LLP Morgan Chu (70446)	
13			Jonathan Steinberg (98044) Benjamin Hattenbach (186455)	
14			D / /	
15			By: /s/ Benjamin Hattenbach (186455)	
16			Attorneys for Plaintiff TESSERA, INC.	
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1	1 <u>DEMAND FOR JURY</u>	<u>FRIAL</u>
2	Tessera hereby demands a trial by jury on all issue.	s triable to a jury.
3	3 DATED: December 16, 2005 Respectfully	Submitted,
4		
5	5 IRELL & MA Morgan Chu	ANELLA LLP (70446)
6	6 Jonathan Ster Benjamin Ha	(70446) nberg (98044) ttenbach (186455)
7		
8	By:	/s/ ttenbach (186455) Plaintiff NC.
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TESSERA'S FIRST AMENDED COMPLAINT FOR PATENT INFRINGEMENT